09-19-01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application of:

Christophe J. Chevallier

SUPPLY VOLTAGE REDUCTION CIRCUIT FOR INTEGRATED CIRCUIT

Attorney Docket No.: 703.019US3



PATENT APPLICATION TRANSMITTAL

BOX PATENT APPLICATION

Commissioner for Patents Washington, D.C. 20231

-We are transmitting herewith the following attached items and information (as indicated with an "X"):

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- CONTINUATION of prior Patent Application No. <u>09/448,881</u> (under 37 CFR § 1.53(b)) comprising: X
 - Specification (17 pgs, including claims numbered 1 through 21 and a 1 page Abstract).
 - <u>X</u> Formal Drawing(s) (<u>8</u> sheets).
 - X Copy of signed Combined Declaration and Power of Attorney (3 pgs) from prior application.
 - Incorporation by Reference: The entire disclosure of the prior application, from which a copy of the oath or <u>X</u> declaration is supplied herewith, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.
 - Check in the amount of \$710.00 to pay the filing fee.
- Prior application is assigned of record to Micron Technology, Inc. .
- XIII Information Disclosure Statement (1 pgs), Form 1449 (1 pgs). References NOT enclosed, cited in prior application.
- Preliminary Amendment (2 pgs).
- Copy of Communication Re: Certificate of Merger from prior application (6 pgs.).

The filing fee has been calculated below as follows:

To the second se	No. Filed	No. Extra	Rate	Fee
TOTAL CLAIMS	3 - 20 =	0	x 18 =	\$0.00
NDEPENDENT CLAIMS	3 - 3 =	0	x 80 =	\$0.00
[] MULTIPLE DEPENDENT CLAIMS PRESENTED				\$0.00
BASIC FEE				\$710.00
TOTAL				\$710.00

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This paper or fee is being deposited on the date indicated above with the United States Postal Service pursuant to 37 CFR 1.10, and is addressed to The Commissioner for Patents, Box Patent Application, Washington, D.C. 20231.